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Datum/Date

16.10.03

Zeichen/Ref/Réf. 01754-03 La/bz	Anmeldung Nr./Application No./Demande n°./Patent Nr./Patent No./Brevet n°. 03014110.5-2220-
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Anmelder/Applicant/Demandeur/Patentinhaber/Proprietor/Titulaire  
Murata Manufacturing Co., Ltd.

## COMMUNICATION

The European Patent Office herewith transmits as an enclosure the European search report for the above-mentioned European patent application.

If applicable, copies of the documents cited in the European search report are attached.

Additional set(s) of copies of the documents cited in the European search report is (are) enclosed as well.

The following specifications given by the applicant have been approved by the Search Division:

abstract

title

The abstract was modified by the Search Division and the definitive text is attached to this communication.

The following figure will be published together with the abstract:

1



## REFUND OF THE SEARCH FEE

If applicable under Article 10 Rules relating to fees, a separate communication from the Receiving Section on the refund of the search fee will be sent later.

**ANNEX TO THE EUROPEAN SEARCH REPORT  
ON EUROPEAN PATENT APPLICATION NO.**

EP 03 01 4110

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on. The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

01-10-2003

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Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
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A	US 4 789 846 A (IYAMA YOSHITADA ET AL) 6 December 1988 (1988-12-06) * column 7, line 34 - column 8, line 54 * * figures 5A,5B,6 * * abstract * ---	1,3,11	
The present search report has been drawn up for all claims		-	
1	Place of search  MUNICH	Date of completion of the search  1 October 2003	Examiner  von Walter, S-U
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons ..... & : member of the same patent family, corresponding document	
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			



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Office

## EUROPEAN SEARCH REPORT

Application Number

EP 03 01 4110

### DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
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The present search report has been drawn up for all claims			
1	Place of search  MUNICH	Date of completion of the search  1 October 2003	Examiner  von Walter, S-U
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			
T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons ..... & : member of the same patent family, corresponding document			



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## ABSTRACT / ZUSAMMENFASSUNG / ABREGE

03014110.5

A high-frequency switch (10) comprises: a substrate (11); a main line electrode (12) provided between two terminals; a stub line electrode (15) with one end thereof connected to the side edge of the main line electrode and the other end thereof grounded; and a ground electrode (16) provided adjacent to the stub line electrode in the width direction thereof; wherein the substrate has a semiconductor activation layer (19) which extends to below the stub line electrode (15) and the ground electrode (16) between at least one side edge of the stubline electrode and the ground electrode; and wherein a gate electrode (20) which extends in the longitudinal direction of the stub line electrode is provided on the semiconductor activation layer between the stub line electrode and the ground electrode, thereby forming an FET structure, thus providing a high-frequency switch and electronic device therewith, capable of using high frequencies, having reduced insertion loss, and high signal cutoff capabilities.



P/1071-1595

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of  
Hiroyuki NAKANO  
Serial No.: 10/601,799  
Filed: June 23, 2003

New York, New York  
Date: October 27, 2003  
Group Art Unit: ---  
Examiner: ---

For: HIGH-FREQUENCY SWITCH, AND ELECTRONIC DEVICE USING THE SAME

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

SUBMISSION

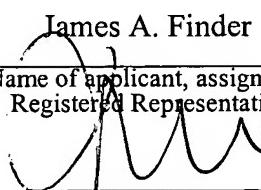
Sir:

Submitted herewith is a copy of a search report issued by a patent searching authority other than the PTO and the cited art together with a form listing the same for the convenience of the Examiner.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents P.O. Box 1450, Alexandria, VA 22313-1450, on October 27, 2003

James A. Finder  

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Name of applicant, assignee or  
Registered Representative  
  

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Signature  
October 27, 2003  

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Date of Signature

Respectfully submitted,



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Enclosures